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L26: Entry 1 of 14

File: USPT

Mar 16, 2004

DOCUMENT-IDENTIFIER: US 6705524 B2

TITLE: Methods and apparatus for preventing damage to memory cards, memory card connectors and other electronic devices

Brief Summary Text (12):

The inventors herein have determined that the conventional method checking the logic signals associated with the nCD1 and nCD2 contacts in a Compact Flash memory card is susceptible to improvement because the conventional method will not detect when the memory card has been inserted into its intended I/O port upside down. This is because the nCD1 and nCD2 contacts are symmetrically located on the memory card. As such, when the memory card is inserted into the I/O port upside down, both of the card detect lines will be connected to a grounded contact on the memory card (albeit the wrong one) and the firmware or hardware will see logic "0" signals on each of the card detect lines. Card based operations will then be enabled, which can result in damage to the memory card or host device.

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L26: Entry 2 of 14

File: USPT

Mar 4, 2003

DOCUMENT-IDENTIFIER: US 6529967 B1

TITLE: System for monitoring and detecting difference voltage levels of I/O adapter cards via a connector

## CLAIMS:

1. A data processing system that performs operations at a plurality of operating voltages, comprising: a central processing unit; a plurality of connectors coupled to said central processing unit for receiving at least one of a first adapter card operating at a specific first voltage and including a reference contact for providing a first reference voltage to a means for detecting, within said connectors, when said first adapter card is inserted in said connector and a voltage contact for providing said first operating voltage when said first adapter card is inserted in said connector, a second adapter operating at a specific second voltage, different from said specific first voltage, and including a reference contact for providing a second reference voltage when said second adapter card is inserted in said connector and a voltage contact for providing said second operating voltage when said second adapter card is inserted in said connector, and a third adapter card for operating at either said first or second operating voltage; means, within said connectors, for detecting the operating voltage of one of said first, second, or third adapter cards inserted in said connector; and means for enabling said operations performed by said data processing system when said operating voltage is compatible with one of the first or second operating voltages wherein said data processing system is enabled when said connectors include at least one said first adapter card, at least one said second adapter card, at least one said third adapter card, in combination with at least one of either said first adapter card or said second adapter card.

8. A method of processing data in a system that operates at a plurality of operating voltages, said method comprising the steps of: providing a central processing unit; coupling, to said central processing unit, a plurality of connectors for receiving at least one of a first adapter card operating at a specific first voltage and including a reference contact for providing a first reference voltage to a means for detecting, within said connectors, when said first adapter card is inserted in said connector and a voltage contact for providing said first operating voltage when said first adapter card is inserted in said connector, a second adapter operating at a specific second voltage, different from said specific first voltage, and including a reference contact for providing a second reference voltage when said second adapter card is inserted in said connector and a voltage contact for providing said second operating voltage when said second adapter card is inserted in said connector, and a third adapter card for operating at either said first or second operating voltage; detecting, by said connectors, the operating voltage of one of said first, second, or third adapter cards inserted in said connector; and enabling operation of said data processing system when said operating voltage is compatible with one of said plurality of operating voltages; wherein said data processing system is enabled when said connectors include at least one said first adapter card, at least one said second adapter card, at least one said third adapter card, or at least one said third adapter card in combination with at least one of either said first adapter card or said second adapter card.

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L26: Entry 9 of 14

File: USPT

Apr 29, 1997

DOCUMENT-IDENTIFIER: US 5625593 A

TITLE: Memory card circuit with separate buffer chips

Detailed Description Text (8):

First, the operation mode 1 will be described. The card insertion/detachment signal inputted from the terminal unit to the signal line 23 is raised to "H" level, so that it is applied to the power supply voltage detecting circuit 21. The power supply voltage detecting circuit 21 is enabled to operate when the card insertion/detachment signal is at "H" level as described previously. When input of power is applied from the terminal unit to the power supply input line 14 and the voltage attains a prescribed value, the power supply voltage detecting circuit 21 detects that and introduces base current to the transistor 20. Thus, the transistor 20 is conducted. As a result, the power supply voltage supplied to the power supply input line 14 is applied to the internal power supply line 15 and at the same time the power supply voltage detecting circuit 21 raises to "H" level the opening/closing control signal to be provided to the signal line 24. In consequence, the address decoder 3 and the unidirectional three-state buffers 18a and 18b are conducted.

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L26: Entry 12 of 14

File: USPT

May 9, 1978

DOCUMENT-IDENTIFIER: US 4088878 A

TITLE: Static card reader having multiple selectable codes

Detailed Description Text (5):

A pair of two-input NAND gates 128,130 have respective first inputs cross-coupled to the opposing gate output to form a conventional flip-flop, and second inputs respectively connected to the normally closed contact and the normally open contact of switch 27 which has its common contact connected to ground 131. The second inputs of gates 128,130 are also connected to the voltage source through the respective pull-up resistors 133,129. Thus, the output of gate 128 goes low and the output of gate 130 goes high when switch 27 detects insertion of a card 22 into slot 20 (FIGS. 1 and 2). The output of gate 130 is connected to an input 132 of a NAND gate 134, which comprises a second input to output electronics 127, to enable operation of the output electronics only when a card is detected in the reader. The output of gate 128 is connected to register strobe input 56 to strobe the outputs of amplifiers 46-52 into the register when a card is in a position to be read.

## CLAIMS:

4. The improvement set forth in claim 1 further comprising means disposed adjacent said slot for detecting insertion of a card into said slot and means responsive to said detecting means for enabling operation of said reader only when a card has been inserted into said slot.

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DATE: Wednesday, October 13, 2004

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		<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L26	l11 same (enabl\$4 near3 operat\$7)	14
<input type="checkbox"/>	L25	l11 same (provid\$4 near3 service)	4
<input type="checkbox"/>	L24	l11 same (servic\$4 with ((respons\$4 or accord\$4 or depend\$4 or based) near3 specification))	0
<input type="checkbox"/>	L23	l11 same ((provid\$4 near3 service) with ((respons\$4 or accord\$4 or depend\$4 or based) near3 specification))	0
<input checked="" type="checkbox"/>	<del>L22</del>	<del>l19 and l20</del>	3
<input checked="" type="checkbox"/>	<del>L21</del>	<del>L20 same (power adj (supply or source))</del>	5
<input checked="" type="checkbox"/>	<del>L20</del>	<del>(maximum near2 delay) with (critical adj path)</del>	63
<input checked="" type="checkbox"/>	<del>L19</del>	<del>(adjust\$7 near3 power near3 (supply or source))</del>	9430
<input type="checkbox"/>	L18	L15.clm.	2
<input type="checkbox"/>	L17	L15.ab.	26
<input type="checkbox"/>	L16	l9 and L15	0
<input type="checkbox"/>	L15	L11 same ((start\$4 or turn\$4) near3 (power near2 (supply or source)))	48
<input type="checkbox"/>	L14	L11 same ((start\$4 or turn\$4) near5 power)	88
<input type="checkbox"/>	L13	L11 same ((start\$4 or turn\$4) near5 controller)	37
<input type="checkbox"/>	L12	l9 and L11	44
<input type="checkbox"/>	L11	((detect\$4 or sens\$4) near5 (card or board) near5 (plug\$7 or inser\$7))	4039
<input type="checkbox"/>	L10	l3 and L9	9
<input type="checkbox"/>	L9	l6 or l7 or L8	3322
<input type="checkbox"/>	L8	713/300,320,340.ccls.	1519
<input type="checkbox"/>	L7	713/2,100.ccls.	1396
<input type="checkbox"/>	L6	709/221.ccls.	529
<input type="checkbox"/>	L5	L4.ab.	3
<input type="checkbox"/>	L4	L3 same (detect\$4 or sens\$4)	16
<input type="checkbox"/>	L3	L1 same (control\$4 near2 power near2 (supply or source))	76
<input type="checkbox"/>	L2	L1 same (bus adj slot) same (bus adj controller)	11
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L18: Entry 2 of 2

File: USPT

May 16, 2000

DOCUMENT-IDENTIFIER: US 6062480 A

TITLE: Hot docking system and methods for detecting and managing hot docking of bus cards

## CLAIMS:

5. A hot docking system in accordance with claim 1 wherein said card insertion start detector comprises a power supply load detector for determining that a power supply connection of a bus card has electrically coupled to a corresponding power supply connection on said bus.

6. A hot docking system in accordance with claim 4 wherein said card insertion start detector comprises a power supply load detector for determining that a power supply connection of a bus card has electrically coupled to a corresponding power supply connection on said bus.

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L18: Entry 1 of 2

File: USPT

Oct 3, 2000

DOCUMENT-IDENTIFIER: US 6128511 A

TITLE: Card-equipped portable telephone with a security feature

## CLAIMS:

1. A portable telephone set comprising:

a card having a first memory in which identification information and a password of a subscriber are stored in advance;

a portable telephone set body having at least radio means for transmitting/receiving a radio signal, and adapted to perform originating and terminating operations;

a card insertion detecting means for detecting insertion of said card in said portable telephone set body;

control means for setting said portable telephone set body in a password input wait state when insertion of said card is detected by said card insertion detecting means and a power supply of said portable telephone set is turned on while said card is inserted therein, and for setting said portable telephone set body in a wait state allowing said originating and terminating operations only when a correct password is input in the password input wait state; and

power control means for turning on a power supply of said control means in accordance with a detection signal from said card insertion detecting means.

15. A portable telephone set comprising:

a card having a first memory in which identification information and a password of a subscriber are stored in advance;

a portable telephone set body having a transmitter/receiver of a radio signal, and being adapted to perform originating and terminating operations;

a card insertion detector which detects insertion of said card in said portable telephone set body;

a controller which sets said portable telephone set body in a password input wait state when insertion of said card is detected by said card insertion detector and a power supply of said portable telephone set is turned on while said card is inserted therein, and sets said portable telephone set body in a wait state allowing said originating and terminating operations only when a correct password is input in the password input wait state; and

a power controller which turns on a power supply of said controller in accordance with a detection signal from said card insertion detector.